

ABSTRACT OF THE DISCLOSURE

INTERRUPT CONTROLLER WITH PREAMBLE EXECUTION AND DISABLE CONTROL BIT

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A interrupt controller includes specialized interfaces and controls for ARM7TDMI-type microcontroller cores. Such sends interrupt vectors and IRQ or FIQ interrupt requests to the processor depending on particular interrupts received.

10 Wherein, THUMB program execution is more economical with program code space, and an interrupt service routine preamble is coded in ARM program code to cause a switch to THUMB program execution. The interrupt service routine preamble is shared amongst all the interrupt service routines to further
15 economize on program code space.